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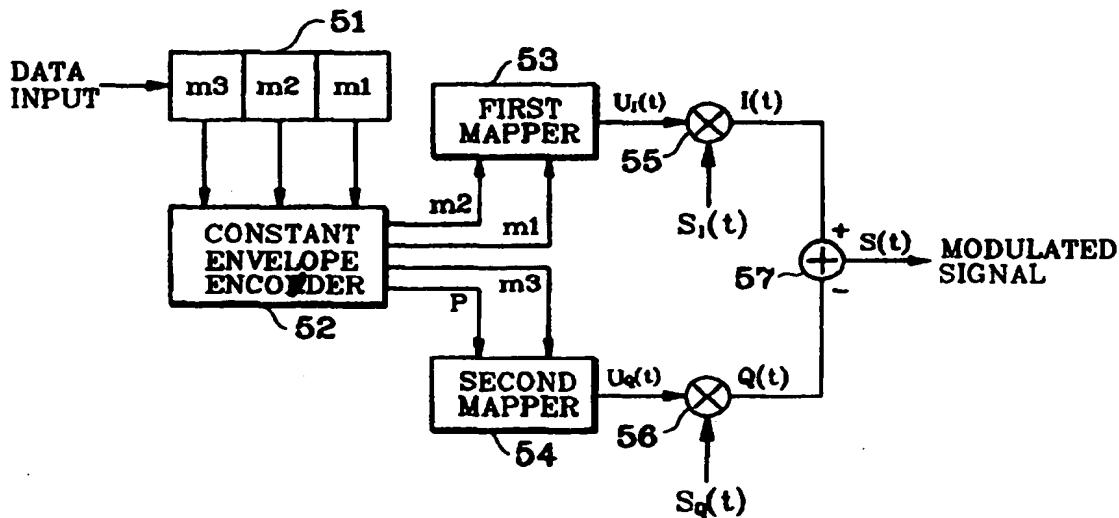
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(54) Title: METHOD AND APPARATUS FOR CONSTANT ENVELOPE QUADRATURE AMPLITUDE MODULATION



(57) Abstract

A constant envelope quadrature amplitude modulation system is directed to solve the bandwidth efficiency problem resulting from the lack of frequency resource, implementing the constant envelope characteristic for the non-linear communication system, based on 16-QAM (Quadrature Amplitude Modulation) system. The present invention limits the data, which will be modulated and transmitted, to 3-bit and adds a parity bit to make them have the constant envelope in the 16-QAM system, which modulates data to be transmitted in the unit of 4-bit. The data value of the parity bit is decided to satisfy the condition of constant envelope in accordance with the QAM signals.

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**METHOD AND APPARATUS FOR CONSTANT ENVELOPE QUADRATURE
AMPLITUDE MODULATION**

Technical Field

The present invention relates to a quadrature amplitude modulation (QAM). Specifically, this invention is a method and apparatus for generating a signal having a constant envelope in a 16 QAM system.

Background Art

As information is getting important in the recent society, many communication terminals, such as a personal communication terminal and a mobile communication terminal, have been developed and spread. Since these communication terminals usually operate digitally, and transmit data through radio, they employ a digital modulation system which mixes digital data with a specified frequency signal, such as sine or cosine wave in signal transmission.

The conventional digital modulation system employed by a mobile or personal communication system is a phase shift keying (PSK) system or a quadrature PSK (QPSK) system. Under the present communication environment where frequency resource is limited, the modulation method described above cannot

satisfy the requirements of high speed transmission and mass information transmission, such as image data transmission.

To solve these problems, a QAM system has been developed to obtain higher bandwidth efficiency, compared to the existing PSK or QPSK.

FIG. 1 is a block diagram of a 16 QAM system according to prior art. Serial-to-parallel converter 11 receives 4-bit data in serial and outputs them in parallel. First mapper 12 converts 2-bit data (I , \bar{I}) transmitted from serial-to-parallel converter 11, into level signals corresponding to the relevant data values. Second mapper 13 converts 2-bit data (Q , \bar{Q}), transmitted from serial-to-parallel converter 11, into level signals corresponding to the relevant data values. First and second mappers, 12 and 13, generate specified level signals as the following Table 1;

[Table 1]

I (Q)	\bar{I} (\bar{Q})	Output (V)
0	0	-1
0	1	-3
1	0	+1
1	1	+3

First mixer 14 mixes the level signal generated by first mapper 12 with signal $\cos\omega_0 t$ having a predetermined frequency (ω_0). Second mixer 15 mixes the level signal generated by

second mapper 13 with signal $\sin\omega_0 t$, which has the same frequency (ω_0) as the signal $\cos\omega_0 t$ and a phase difference of 90° . Adder 16 sums up the I channel signal, [I(t)], and the Q channel signal, [Q(t)], generated by first and second mixer,
5 14 and 15, respectively.

In the QAM system, 4-bit data are simultaneously modulated. When expressing a signal interval corresponding to one bit data as a Tb, each data bit is output for 4-Tb. During that time, the data of 4 bits is input to serial-to-parallel
10 converter 11.

FIG. 2 is a graph illustrating a frequency spectrum of a signal output by the QAM system. When the period of the signal is T, the bandwidth of transmitting and receiving frequency is set to $1/T$. The bandwidth of a signal according to the above
15 QAM system becomes $1/(4Tb)$. Therefore, the bandwidth efficiency in the QAM system is double $1/(2Tb)$ in the QPSK system, or quadruple $1/Tb$ in the PSK system.

FIG. 3 is a block diagram of a receiver for receiving and demodulating the quadrature amplitude modulated signal through
20 the above operations. Third mixer 31 mixes the received QAM signal with signal $\cos\omega_0 t$ having a predetermined frequency (ω_0). Fourth mixer 32 mixes the received QAM signal with signal $\sin\omega_0 t$, which has the same frequency (ω_0) as the signal $\cos\omega_0 t$ and a phase difference of 90° . First integrator 33 integrates

the signal generated by third mixer 31. Second integrator 34 integrates the signal generated by fourth mixer 32. First demapper 35 determines the level of the integrated signal generated by first integrator 33 and generates 2-bit data corresponding to the relevant levels. Second demapper 36 determines the level of the integrated signal generated by second integrator 34 and generates 2-bit data corresponding to the relevant levels.

Parallel-to-serial converter 37 receives the data bits from first and second demappers 35 and 36 in parallel and outputs them in serial. The configuration shown in FIG. 3 receives a signal, for example, $\cos\omega_0 t - \sin\omega_0 t$, transmitted from the QAM system. The input signal becomes $\cos^2\omega_0 t - \cos\omega_0 t \cdot \sin\omega_0 t$ after passing through mixer 31. The signal is then integrated during one symbol period (T_b) by first integrator 33. The integrated signal is expressed as

$$\int_0^{T_b} \cos^2\omega_0 t dt - \int_0^{T_b} \cos\omega_0 t \cdot \sin\omega_0 t dt. \text{ This is newly expressed as}$$

[Formula 1]

$$\int_0^{T_b} \frac{1}{2} dt + \int_0^{T_b} \frac{1}{2} \cos 2\omega_0 t dt - \int_0^{T_b} \cos\omega_0 t \cdot \sin\omega_0 t dt.$$

Values of $\int_0^{T_b} \frac{1}{2} \cos 2\omega_0 t dt$ and $\int_0^{T_b} \cos\omega_0 t \cdot \sin\omega_0 t dt$ are "0", thus the output signal from first integrator 35 is $\int_0^{T_b} \frac{1}{2} dt$.

First integrator 33 and second integrator 34 generate

level signals of $\pm A$ (A is a specified value) through the above processes. The generated signals are applied to first and second demappers 35 and 36. The demappers 35 and 36 demodulate the signals to restore original data.

5 However, the conventional QAM system described above has the following problems.

When the data which is input to the modulator is "0000 1011 0110" in the QAM system, signal, $[I(t)]$, which is output by first mixer 14, is shown in FIG. 4A, and signal, $[Q(t)]$, which is output by second mixer 15, is shown in FIG. 4B. 10 Signal, $[S(t)]$, which is output by adder 16, has different amplitudes according to data to be modulated, as shown in FIG. 4C.

For radio communication, since it is necessary to amplify 15 the level of a signal forwarded through an antenna, a high power amplifier must be installed at the front stage of the antenna. Especially, since data must be transmitted between a land station and an artificial satellite, a high power amplifier must be installed at the output stage in a satellite 20 communication system.

Usually a class C amplifier is employed as the high power amplifier to increase electric power efficiency. Since an input-to-output characteristic in the class C amplifier is non-linear, the phase is deviated in accordance with the

change of amplitude signal when the amplitude of an input signal changes. This deteriorates the performance of the system. Therefore, the input signal to the high power amplifier, such as a class C amplifier, must have a constant envelope.

5

However, since the amplitudes of output signals change according to output data in the conventional QAM system, the QAM system cannot be used in a non-linear communication system even though it has high bandwidth efficiency.

10 Disclosure of Invention

Accordingly, the present invention is directed to a method and apparatus for a QAM having a constant envelope that substantially obviates one or more of the limitations and disadvantages of the related art.

15 An object of the present invention is to provide a method and apparatus for a constant envelope QAM (CE-QAM).

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by 20 practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure as illustrated in the written description and claims hereof, as well as the appended drawings.

To achieve these and other advantages, and in accordance with the purpose of the present invention as embodied and broadly described, a method for a QAM includes the steps of: inputting 3-bit data; adding a single parity bit to the 5 information bits to make 4-bit data have a constant envelope; converting the 4-bit data into corresponding first and second level signals by 2-bit; mixing the first level signal with a first frequency signal; mixing the second level signal with a second frequency signal having a predetermined phase difference against the first frequency signal; and adding up 10 signals obtained through two mixing steps.

An apparatus for a QAM consists of: an encoding means for adding a single parity bit to 3-bit of input information; first and second mapping means for respectively generating 15 level signals corresponding to 2-bit of data values among 4-bit generated by the encoding means; a first mixer for mixing the level signal generated by the first mapping means with a first frequency signal; a second mixer for mixing the level signal generated by the second mapping means with a second frequency signal; and an adding means for adding up the 20 signals generated by the first and second mixers.

The first and second mapping means respectively convert 2-bit input data into at least one level signal among the values of -1.21, +1.21, -2.92, and +2.92.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

5

Brief Description of Drawings

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate 10 embodiments of the invention and together with the description serve to explain the principles of the invention.

In the drawings:

FIG. 1 is a block diagram of a usual 16 QAM system;

FIG. 2 is a graph showing bandwidth efficiency of a QAM 15 system;

FIG. 3 is a block diagram of a demodulator for demodulating 16 QAM signals;

FIG. 4A through 4C are waveforms of modulated signals showing the problems of the conventional QAM system;

20 FIG. 5 is a block diagram of an apparatus for a 16 CE-QAM system according to the present invention;

FIG. 6 illustrates an exemplary embodiment of the constant envelope encoder shown in FIG. 5;

FIGS. 7A through 7C illustrate output waveforms of the

CE-QAM according to the present invention;

FIG. 8 is a block diagram of a demodulator according to the present invention;

FIGS. 9 and 10 are constellation points diagrams of CE-
5 QAM system according to the present invention; and

FIG. 11 is a graph for explaining a symbol error rate in
the CE-QAM system according to the present invention.

Best Mode for Carrying Out the Invention

Reference will now be made in detail to the preferred
10 embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

With reference to the attached drawings, a preferred embodiment of the present invention is described below.

First, basic concept of the present invention is set
15 forth in the following description.

As shown in FIG. 1, modulated signal, [S(t)], which is output by adder 16, is given by

[Formula 2]

$$S(t) = U_1(t)\cos\omega_0t - U_0(t)\sin\omega_0t.$$

20 This is expressed as the following formula.

[Formula 3]

$$S(t) = A(t) \sin(\omega_0 t + \theta(t)),$$

where amplitude, $A(t)$, and phase, $\theta(t)$, are given by the following formulas 4 and 5, respectively.

5

[Formula 4]

$$A(t) = \sqrt{U_I(t)^2 + U_Q(t)^2}$$

[Formula 5]

$$\theta(t) = \tan^{-1} \left[-\frac{U_Q(t)}{U_I(t)} \right]$$

To make output signal, [S(t)], which is output by the QAM 10 system, have a constant envelope, the amplitude, [A(t)], expressed as the Formula 3, must have a constant value. Therefore, the following conditions for a constant envelope are derived from the Formula 3:

[Formula 6]

$$15 \quad A(t) = \sqrt{U_I(t)^2 + U_Q(t)^2} = \text{Constant},$$

or

[Formula 7]

$$U_I(t)^2 + U_Q(t)^2 = \text{Constant},$$

and

[Formula 8]

$$|U_1(t)| \neq |U_0(t)|.$$

In the 16 QAM system using the level values of -1, -3, +1, and +3, as shown in Table 1, the following eight subsets 5 (U_1, U_0) satisfying the Formulas 7 and 8, are obtained: (-3, -1), (-3, +1), (-1, -3), (-1, +3), (+1, -3), (+1, +3), (+3, -1), (+3, +1). The values of the subsets (U_1, U_0) , are not predetermined, but change according to 4-bit data to be transmitted, as shown in FIG. 1, so it is impossible to 10 satisfy the constant envelope characteristic, using the 4-bit data, which will be modulated.

The present invention limits the data, which will be modulated and transmitted, to 3-bit, and adds one parity bit to make the output signal have the constant envelope in the 16 15 QAM system, which modulates data to be transmitted in the unit of 4-bit.

When the level values corresponding to the data values of the channels I and Q shown in FIG. 1 are established as shown in Tables 2 and 3, when the data of the channel I is fixed as 20 transmission data, and when a parity bit P for the constant envelope characteristic is added to the data of the channel Q, parity bits corresponding to the data to be modulated can be obtained as shown in Table 4.

[Table 2] Level translation of channel I

I	\bar{I}	Output (V)
0	0	-3
0	1	-1
1	0	+1
1	1	+3

[Table 3] Level translation of channel Q

Q	\bar{Q}	Output (V)
0	0	-1
0	1	-3
1	0	+3
1	1	+1

[Table 4] Parity bit and corresponding level translation

I	\bar{I}	Q	P	I Output	Q output
0	0	0	0	-3	-1
0	0	1	1	-3	+1
0	1	0	1	-1	-3
0	1	1	0	-1	+3
1	0	0	1	+1	-3
1	0	1	0	+1	+3
1	1	0	0	+3	-1
1	1	1	1	+3	+1

In Tables 2 through 4, the level translation values of the I channel data and the Q channel data are not limited to

specific examples. The parity bit can also be added to the channel I, not limiting to the channel Q. In any cases, the output signal is made to have the constant envelope in the same way, based on the concept described above.

5 FIG. 5 is a block diagram of a QAM system according to the present invention.

Serial-to-parallel converter 51 receives three serial bits (m_1, m_2, m_3) and outputs them in parallel. Constant envelope encoder 52 adds a predetermined parity bit P to the 10 3-bit data (m_1, m_2, m_3) transmitted by serial-to-parallel converter 51, and generates 4-bit data (m_1, m_2, m_3, p) having the constant envelope characteristic.

First mapper 53 converts the 2-bit data for the channel I, m_1 and m_2 , output by constant envelope encoder 52, into 15 corresponding level signals. Second mapper 54 converts the 2-bit data for the channel Q, m_3 and p , output by constant envelope encoder 52, into corresponding level signals.

First mixer 55 mixes the signal, $[U_I(t)]$, output by first mapper 53, with a predetermined first frequency signal, 20 $[S_I(t)]$. Second mixer 56 mixes the signal, $[U_Q(t)]$, output by second mapper 54, with a predetermined second frequency signal, $[S_Q(t)]$. Cosine wave or sine wave can be used as the first and second frequency signals, $[S_I(t), S_Q(t)]$, as shown in FIG. 1.

Adder 57 adds up the I channel signal, [I(t)], and Q channel signal, [Q(t)], which are respectively output by first and second mixers, 55 and 56.

Constant envelope encoder 52 adds the predetermined parity bit p to the 3-bit data (m1, m2, m3) output by serial-to-parallel converter 51, and generates 4-bit data (m1, m2, m3, p) having the constant envelope characteristic.

In Table 4, a parity bit is obtained by exclusively adding up the values of 3-bit data. Constant envelope encoder 52 outputs the input data bits (m1, m2, m3) as they are, based upon the Table 4, and modulo-two adder 61 adds up 3-bit data (m1, m2, m3) as shown in FIG. 6. Constant envelope encoder 52 is not limited to a specific configuration, but designed to correspond to the levels of signals converted by first and second mappers 53 and 54.

The data is sent to first and second mapper 53 and 54 after being encoded by constant envelope encoder 52. As shown in FIG. 7, the amplitudes of I channel signal, [I(t)], and Q channel signal, [Q(t)], are complementarily established, thus the amplitudes of modulated signals, [s(t)], output through an output stage, become constant.

FIG. 8 is a block diagram of a demodulator for demodulating the modulated signal output by the QAM system.

Third mixer 81 mixes the signal transmitted from QAM

system with a predetermined first frequency signal, $[S_1(t)]$. Fourth mixer 82 mixes the signal transmitted from QAM system with a second frequency signal, $[S_0(t)]$, having a phase difference of 90° against the first frequency signal, $[S_1(t)]$.

5 First integrator 83 integrates a frequency signal output by third mixer 81. Second integrator 84 integrates a frequency signal output by fourth mixer 82.

First demapper 85 generates 2-bit data (m_1, m_2) corresponding to the levels of signals output by first integrator 83. Second demapper 86 generates 2-bit data (m_3, p) corresponding to the levels of signals output by second integrator 84, in which one bit p is a parity bit.

15 Parallel-to-serial converter 87 receives the 3-bit data (m_1, m_2, m_3) output by first and second mappers 85 and 86 in parallel, and sequentially outputs them in serial.

The configuration of the demodulator described above is substantially the same as the conventional demodulator as shown in FIG. 3, but one of the bits output by second mapper 86 is detected as a parity bit in this demodulator according 20 to the present invention. Therefore, the conventional demodulator can be used as it is, thus QAM system according to the present invention can be employed without spending additional expense.

FIG. 9 is a constellation points diagram of CE-QAM system

according to the present invention. Constellation points are positioned at a same distance from the central point, showing the fact that the constant envelope characteristic is implemented.

5 However, in this signal constellation, the distance between constellation points "1010" and "1111" is nearer than the distance between constellation points "1111" and "1100". This limits the system efficiency.

10 According to our research, when establishing the translation level of data bit modulation to ± 1.21 and ± 2.92 , not to conventional values ± 1 and ± 3 , an optimal distribution of constellation points is achieved, thus increasing a minimum distance of about 20.1%, and obtaining a relatively good minimum distance in the ranges of ± 1.18 to ± 1.25 and ± 2.90 to
15 ± 2.95 .

FIG. 10 shows how constellation points are distributed when the translation levels of data bits are limited to ± 2.21 and ± 2.92 . Compared with FIG. 9, constellation points in FIG. 10 are uniformly distributed, thus the minimum distance is
20 increased.

As illustrated above, the present invention can provide a method for a CE-QAM, which is applicable to a non-linear communication system by making the QAM system of high bandwidth efficiency have a constant envelope. In the above

exemplary embodiment, a minimum Hamming distance (H_{min}) increases because of the parity bit which is added for implementing the constant envelope characteristic, thus Euclidean distance, or nonidentification of signal waveforms, 5 increases.

Usually, a symbol error rate (P_E^*) against S/N (signal-to-noise ratio) can be approximately expressed through the following formula.

[Formula 9]

$$10 \quad P_E^* \approx 1 - \int_{\pi/M}^{\pi} \sqrt{\frac{Y}{\pi}} \cos\theta e^{-Y \sin^2\theta} d\theta$$

The Formula 9 becomes

$$P_E^* \approx 1 - \left(1 - \int_{\sqrt{Y} \sin \pi/M}^{\pi} \sqrt{\frac{Y}{\pi}} \cos\theta e^{-u^2} du \right)$$

and is newly expressed as

$$P_E^* \approx \operatorname{erfc} \left(\sqrt{Y} \sin \frac{\pi}{M} \right)$$

15 so the symbol error rate (P_E^*) is given by the following Formula 10.

[Formula 10]

$$P_E^* = 2Q \left(\sqrt{\frac{2E_s}{N_0}} \sin \frac{\pi}{M} \right)$$

where γ is S/N, M is a number of constellation points, and E_s is average symbol energy.

When considering the Hamming distance, the symbol error rate is given by the following Formula 11.

5 [Formula 11]

$$2Q\left(\sqrt{\frac{H_{\min} \times R \times 2E_s}{N_0}} \cdot \sin \frac{\pi}{M}\right)$$

where H_{\min} is a minimum Hamming distance, and R is a coding rate.

In this QAM method according to the present invention,
10 the minimum Hamming distance is 2, and the coding rate is 3/4,
thus the Formula 11 is expressed as the following Formula 12.

[Formula 12]

$$2Q\left(\sqrt{\frac{1.5 \times 2E_s}{N_0}} \cdot \sin \frac{\pi}{M}\right)$$

Euclidean distance according to Formula 12 is 1.22 times, or
15 $\sqrt{1.5}$ times, as great as that according to Formula 10.

FIG. 11 is a characteristic curve diagram showing symbol error rates according to each digital modulation system.

Compared with the conventional 16 QAM system, the 16 CeQAM

system according to the present invention is better by about 3.7dB at SER = 10^{-5} .

As described above, the present invention makes modulated signals to be transmitted have a constant envelope in the 16 QAM system, thereby allowing the QAM system to be applicable to a non-linear communication system. Additionally, in the present invention, a minimum Hamming distance increases through a parity bit which is added to information bits, thereby implementing successful operation in the conventional QAM system.

It will be apparent to those skilled in the art that various modifications and variations can be made in a method and apparatus for a CEQAM of the present invention without deviating from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

Claims:

1. A method for a quadrature amplitude modulation (QAM) which modulates data bits in the unit of 4-bit before transmission, the method adding a parity bit to three information bits.

5
2. The method according to claim 1, wherein the parity bit makes the 3-bit information have a constant envelope characteristic.

3. A method for a QAM comprising the steps of:
10 inputting a predetermined number of information bits;
 adding a predetermined number of parity bits to the information bits, wherein the parity bit makes data bits, where it is contained, have a constant envelope characteristic; and
15 performing the QAM with the data bits to which the parity bit is added.

4. A method for a QAM comprising the steps of:
 inputting 3-bit information;
 adding a parity bit to the information bits to make 4-bit
20 data have a constant envelope characteristic;

converting the 4-bit data into corresponding first and second level signals by 2-bit;

mixing the first level signal with a first frequency signal;

5 mixing the second level signal with a second frequency signal having a predetermined phase difference against the first frequency signal; and

adding up signals obtained through two mixing steps.

5. The method according to claim 4, wherein the first
10 and second level signals respectively have at least one level value among the values of -1, +1, -3, and +3.

6. The method according to claim 4, wherein the first and second level signals respectively have at least one level value among the values of -1.18 to -1.25, +1.18 to +1.25,
15 -2.90 to -2.95, and +2.90 to +2.95.

7. The method according to claim 4 or 6, wherein the first and second level signals respectively have at least one level value among the values of -1.21, +1.21, -2.92, and +2.92.

20 8. An apparatus for a QAM comprising:

a constant envelope encoder (52) for adding a parity bit to 3-bit input information;

first and second mappers (53, 54) for respectively generating level signals corresponding to data values of 2-bit
5 among the 4-bit generated by the constant envelope encoder (52);

a first mixer (55) for mixing the level signal generated by the first mapper (53) with a first frequency signal;

10 a second mixer (56) for mixing the level signal generated by the second mapper (54) with a second frequency signal; and an adder (57) for adding up the signals generated by the first and second mixers (55, 56).

9. The apparatus according to claim 8, wherein the first and second mappers (53, 54) respectively convert 2-bit
15 input data into at least one level signal among values of -1, +1, -3, and +3.

10. The apparatus according to claim 8, wherein the first and second mappers (53, 54) respectively convert 2-bit input data into at least one level signal among the values of
20 -1.18 to -1.25, +1.18 to +1.25, -2.90 to -2.95, and +2.90 to +2.95.

11. The apparatus according to claim 8 or 10, wherein the first and second mappers (53, 54) respectively convert 2-bit input data into at least one level signal among the values of -1.21, +1.21, -2.92, and +2.92.

5 12. The apparatus according to claim 8, wherein the constant envelope encoder (52) generates the parity bit, based upon the 3-bit input information.

10 13. The apparatus according to claim 11, wherein the first mapper (53) converts input 2-bit signals, "00", into a level signal, "-2.92", "01" into "-1.21", "10" into "+1.21", and "11" into "+2.92", and the second mapper (54) converts "00" into "-1.21", "01" into "-2.92", "10" into "+2.92", and "11" into "+1.21"

15 14. The apparatus according to claim 12 or 13, wherein the constant envelope encoder (52) adds the parity bit "1" when the number of data "1", contained in the input information bits is odd, and adds the parity bit "0" when the number of data "1", contained in the input information bits, is even.

20 15. The apparatus according to claim 8 or 9, wherein the

first mapper (53) converts 2-bit signals, "00", into a level signal, "-3", "01" into "-1", "10" into "+1", and "11" into "+3", and the second mapper (54) converts "00" into "-1", "01" into "-3", "10" into "+3", and "11" into "+1".

5 16. The apparatus according to claim 15, wherein the constant envelope encoder (52) adds the parity bit "1" when the number of data "1", contained in the input information bits, is odd, and adds the parity bit "0" when the number of data "1", contained in the input information bits, is even.

10 17. The apparatus according to claim 14, wherein the constant envelope encoder (52) includes an adder for adding the input information bits.

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FIG. 1

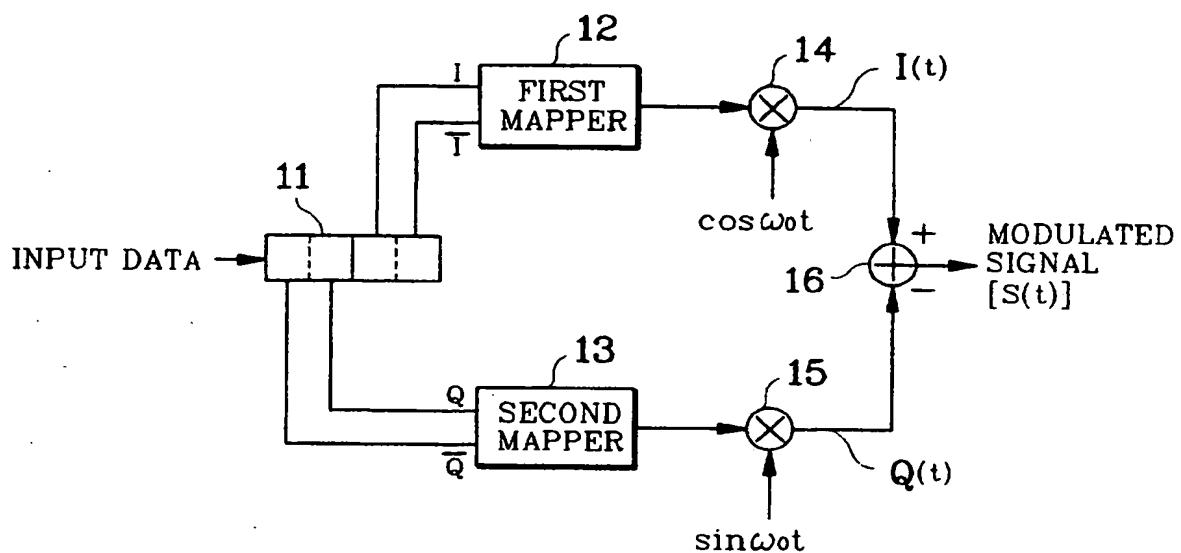
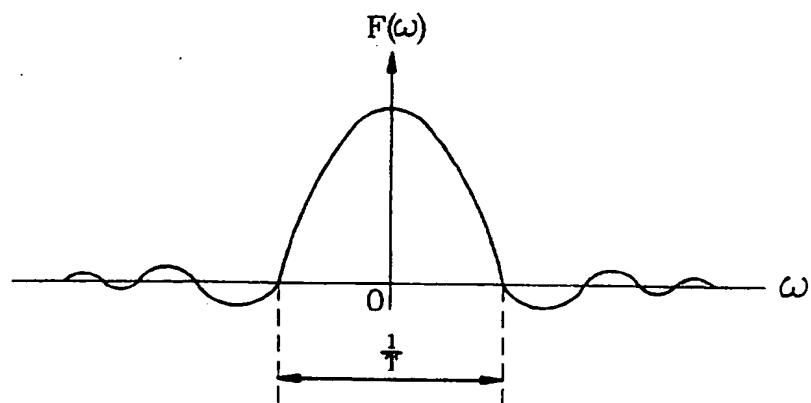
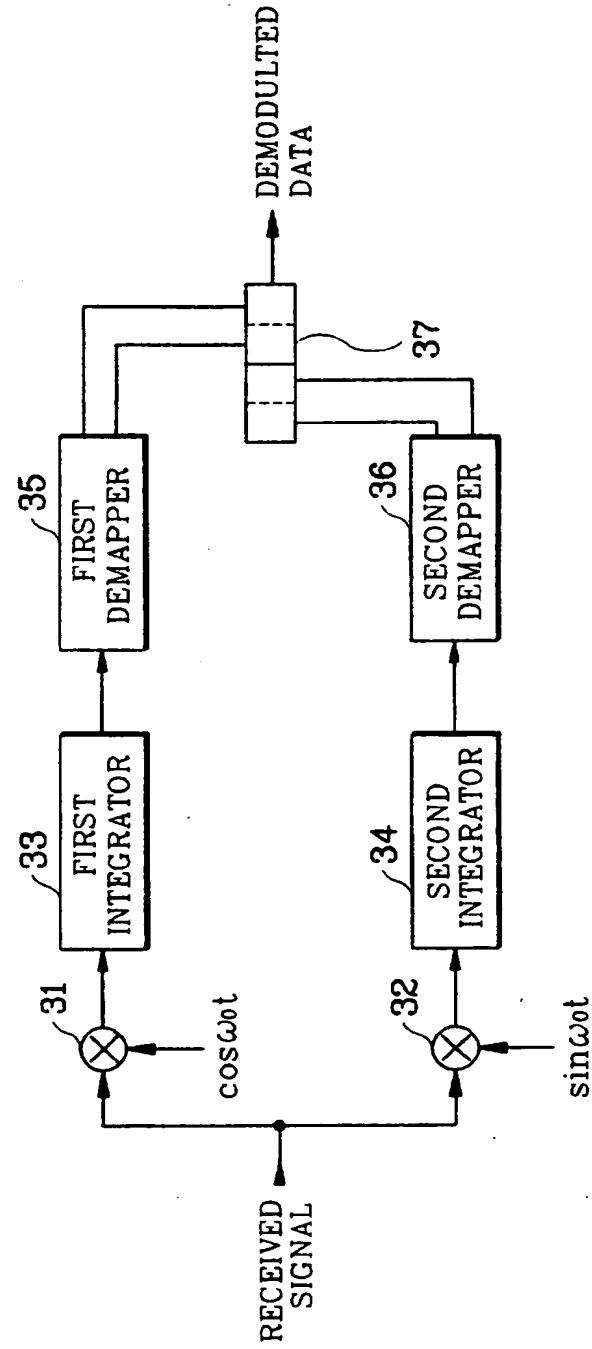


FIG. 2



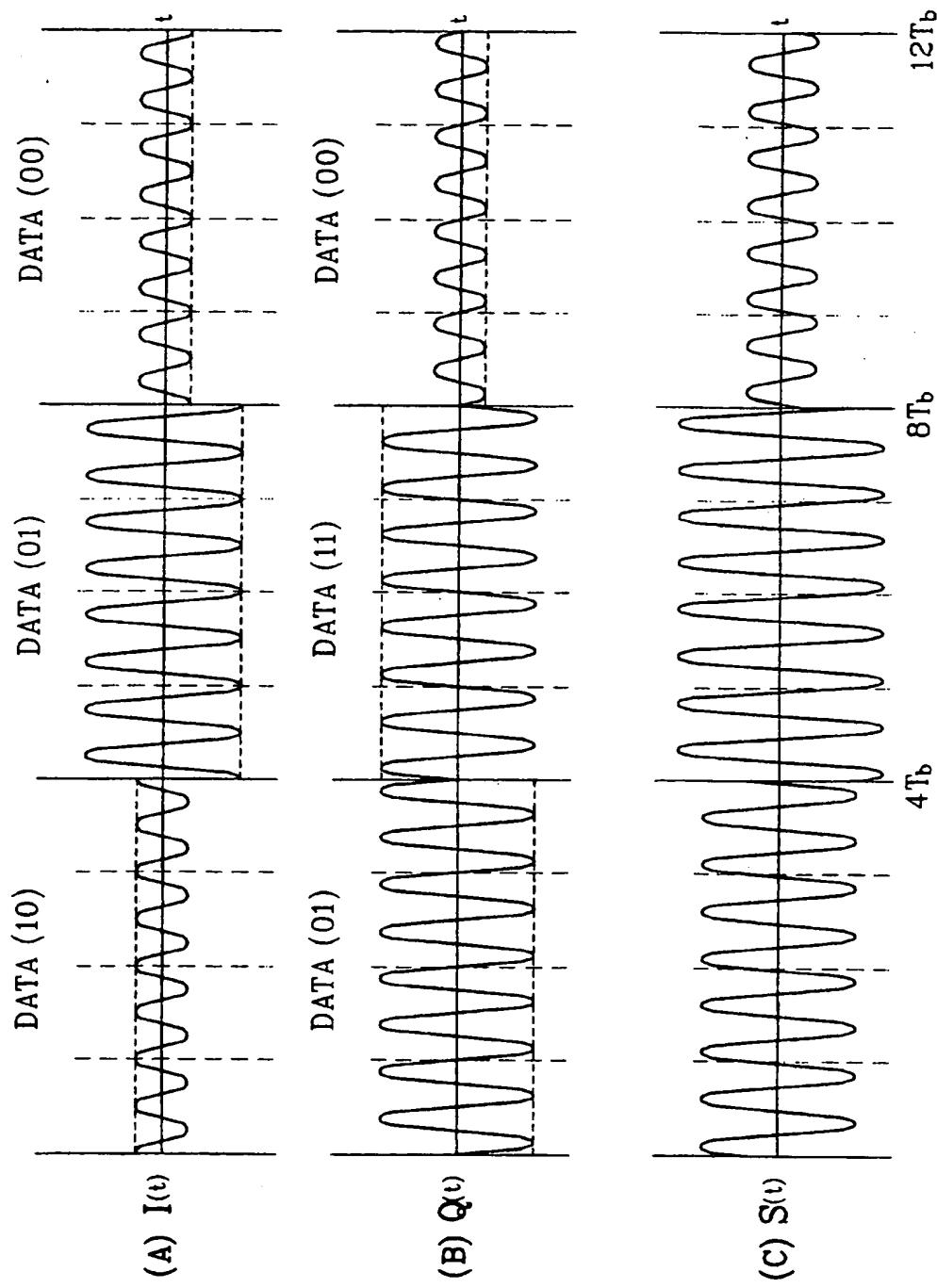
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FIG. 3



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FIG. 4



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FIG. 5

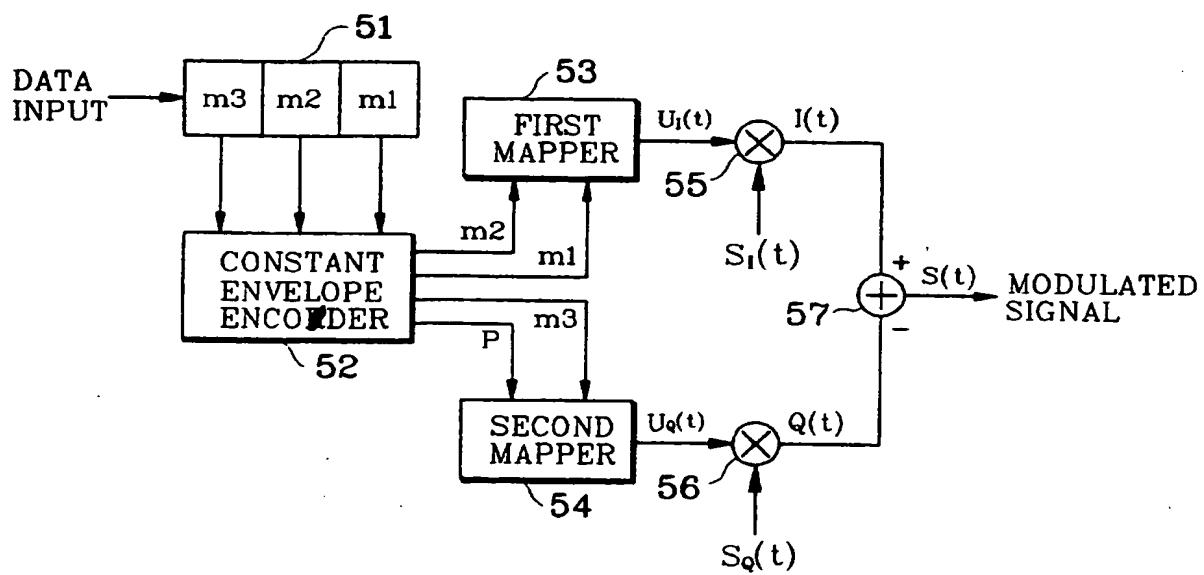
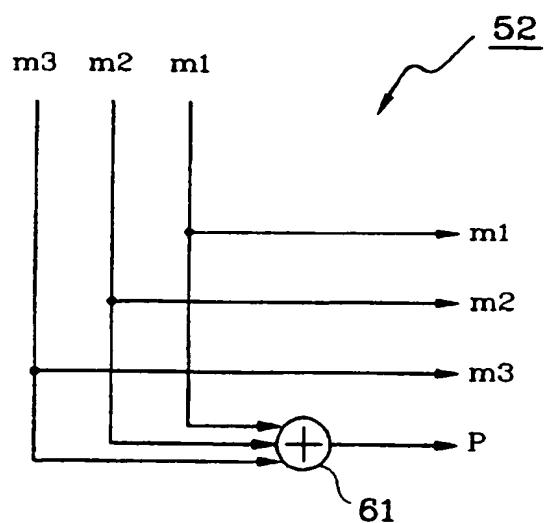
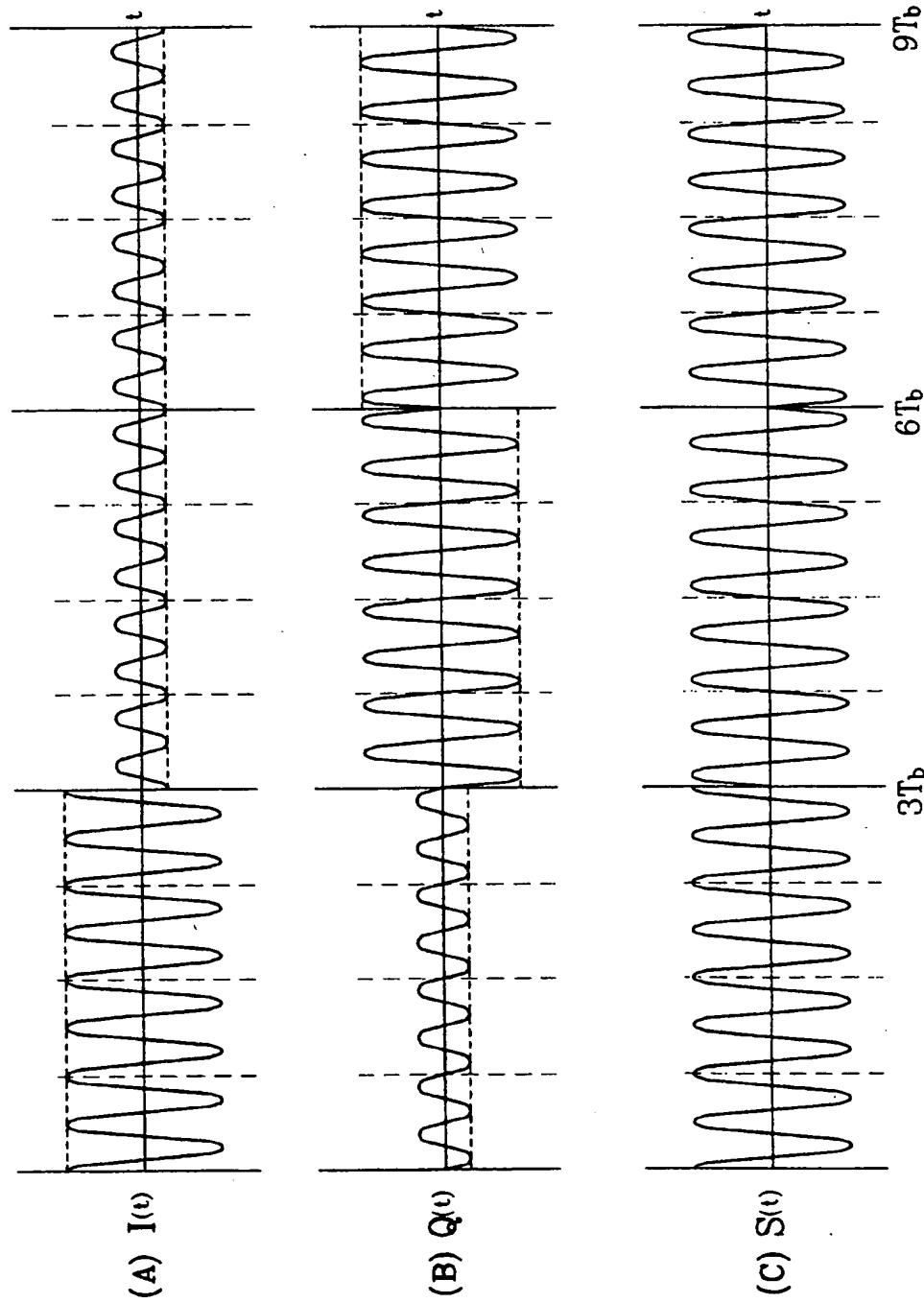


FIG. 6



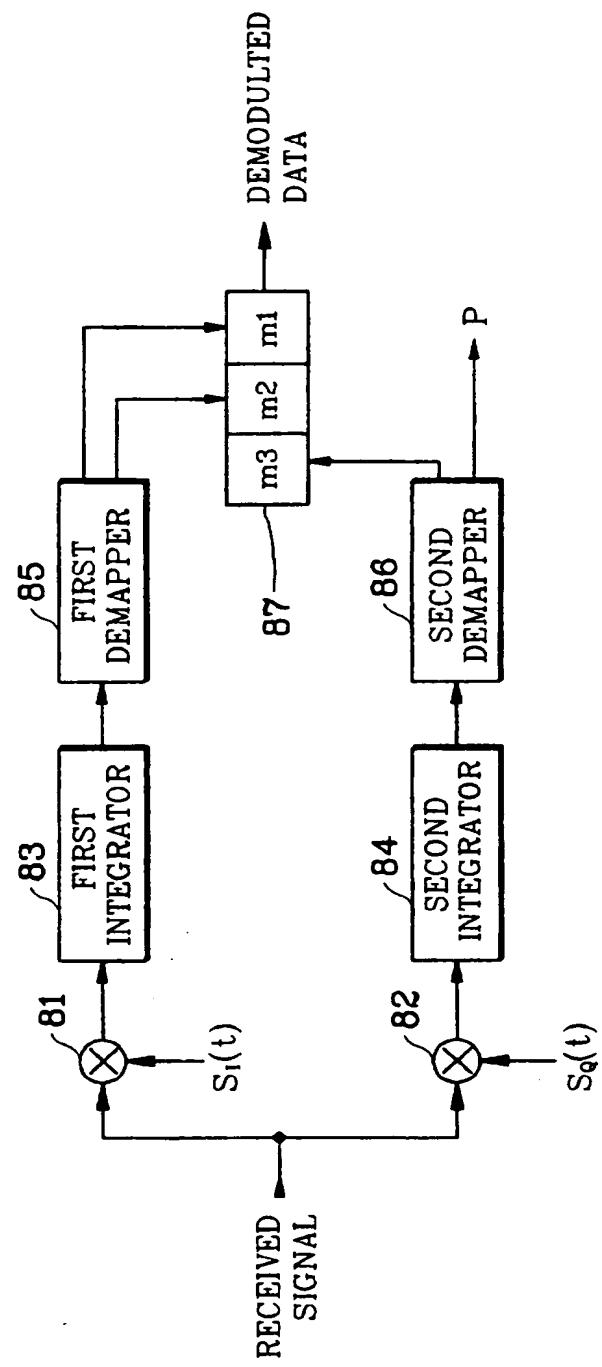
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FIG. 7



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FIG. 8



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FIG. 9

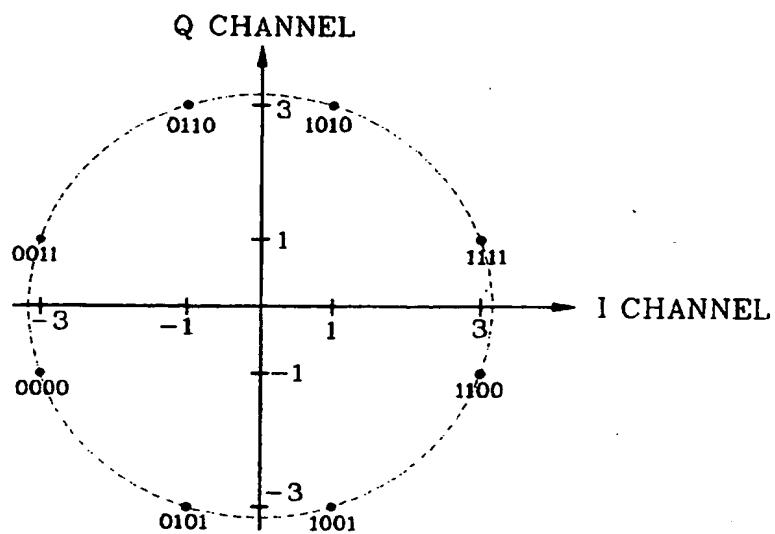
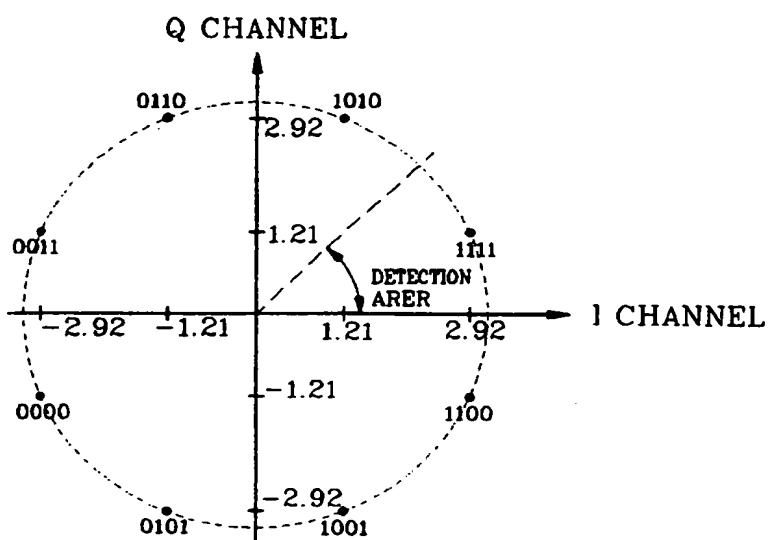
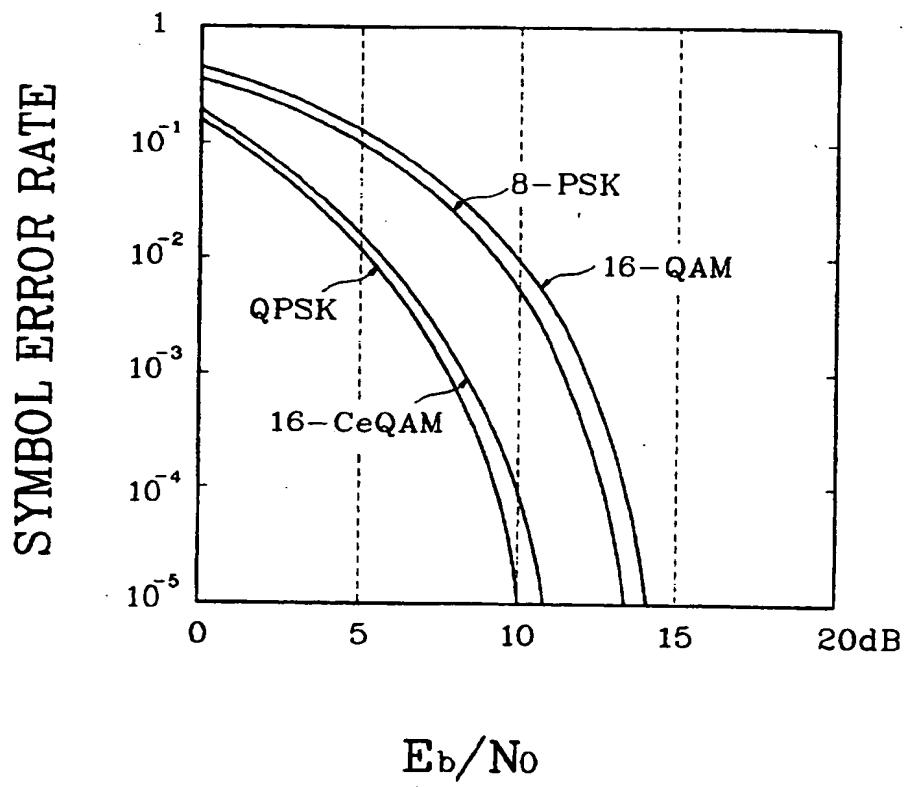


FIG. 10



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FIG. 11



INTERNATIONAL SEARCH REPORT

International application No.

PCT/KR 97/00176

A. CLASSIFICATION OF SUBJECT MATTER

IPC⁶: H 04 L 27/34, 1/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC⁶: H 04 L; H 04 J; H 03 M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	Patent Abstracts of Japan, 22 March 1996 (22.03.96),	1
A	Publication number 8-079325 A (HITACHI).	3,4,8
Y	EP 0 052 463 A1 (PLESSEY) 26 May 1982 (26.05.82),	1
A	page 3, line 14 - page 6, line 3; fig.1-5.	3,4,8
A	Patent Abstracts of Japan, 21 June 1996 (21.06.96),	1,3,4,8
	Publication number 8-163077 A (RICOH).	
A	EP 0 353 694 A2 (SIEMENS) 07 February 1990 (07.02.90), abstract; column 1, line 1 - column 3, line 12. -----	1,3,4,8

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Date of the actual completion of the international search	Date of mailing of the international search report
11 December 1997 (11.12.97)	05 January 1998 (05.01.98)
Name and mailing address of the ISA/AT AUSTRIAN PATENT OFFICE Kohlmarkt 8-10 A-1014 Vienna Facsimile No. 1/53424/535	Authorized officer: Hajos Telephone No. 1/53424/410

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

KR 97/00176

Im Recherchenbericht angeführtes Patentdokument Patent document cited in search report Document de brevet cité dans le rapport de recherche	Datum der Veröffentlichung Publication date Date de publication	Mitglieder(er) der Patentfamilie Patent family member(s) Membre(s) de la famille de brevets	Datum der Veröffentlichung Publication date Date de publication
EP A1 52463	26-05-82	AT E 14961 AU A1 77117/81 AU B2 545194 CA A1 1225743 DE CO 5171834 DK A 5037781 EP B1 52461 ES A1 507129 ES A5 507129 ES A1 8302939 GB A1 2088676 GB B2 2088676 IE B 52428 JP A2 5715024 NO A 813727 NO B 159228 NO C 159228 NZ A 198844 US A 4493082 ZA A 8107711	15-08-85 20-05-85 04-07-85 18-08-85 19-09-85 15-05-85 14-08-85 01-12-85 29-12-85 16-04-86 09-06-86 04-09-85 28-10-85 17-09-85 18-05-85 20-09-85 07-12-85 31-05-84 08-01-85 27-10-82
EP A2 353694	07-02-90	AT E 128303 DE CO 5890944 EP A2 353694 EP B1 353694 US A 5115436	15-10-95 26-10-95 05-02-95 20-08-95 19-05-92

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